AMENDMENTS TO THE DRAWINGS

The attached sheets of drawings include changes to Figs. 56, 58, 62E, 62G, 62H, 62I, 62R, 63F, 64M, and 66J. These sheets, which include Figs. 56, 58, 62E, 62G, 62H, 62I, 62R, 63F, 64M, and 66J, replace the original sheets including Figs. 56, 58, 62E, 62G, 62H, 62I, 62R, 63F, 64M, and 66J. In Fig. 56, the word "where" is replaced by the word "were" in the sentence. In Fig. 58, the word "where" is replaced by the word "were" in the sentence. In Fig. 62E, 32768 kHz is replaced by 32.768 kHz. In Fig. 62G, a missing line segment to connect up to capacitor C49 has been added. In Fig. 62H, a line has been added atop CS10 on the pin 10 designation and QSO has been replaced by DSO on the pin 77 designation. In Fig. 62I, the pin 69 designation has been changed from PSC0/SS/PQS3 to PSC1/PQS4, the pin 68 designation has been changed from TXD\PQS7 to PSC0/SS/PQS3 (with a line above the SS), and the pin 75 designation has been changed from ADDR0 to TXD/PQS7. In Fig. 62R, a line has been added over VFD_CE. In Fig. 63F, MEMA (with a line thereover) has been replaced by MEMR (with a line thereover). In Fig. 64M, a line segment connecting up the output of the lowermost amplifier (designated as U29) with the associated node has been added. In Fig. 66 J, "M SO" has been replaced with MISO.

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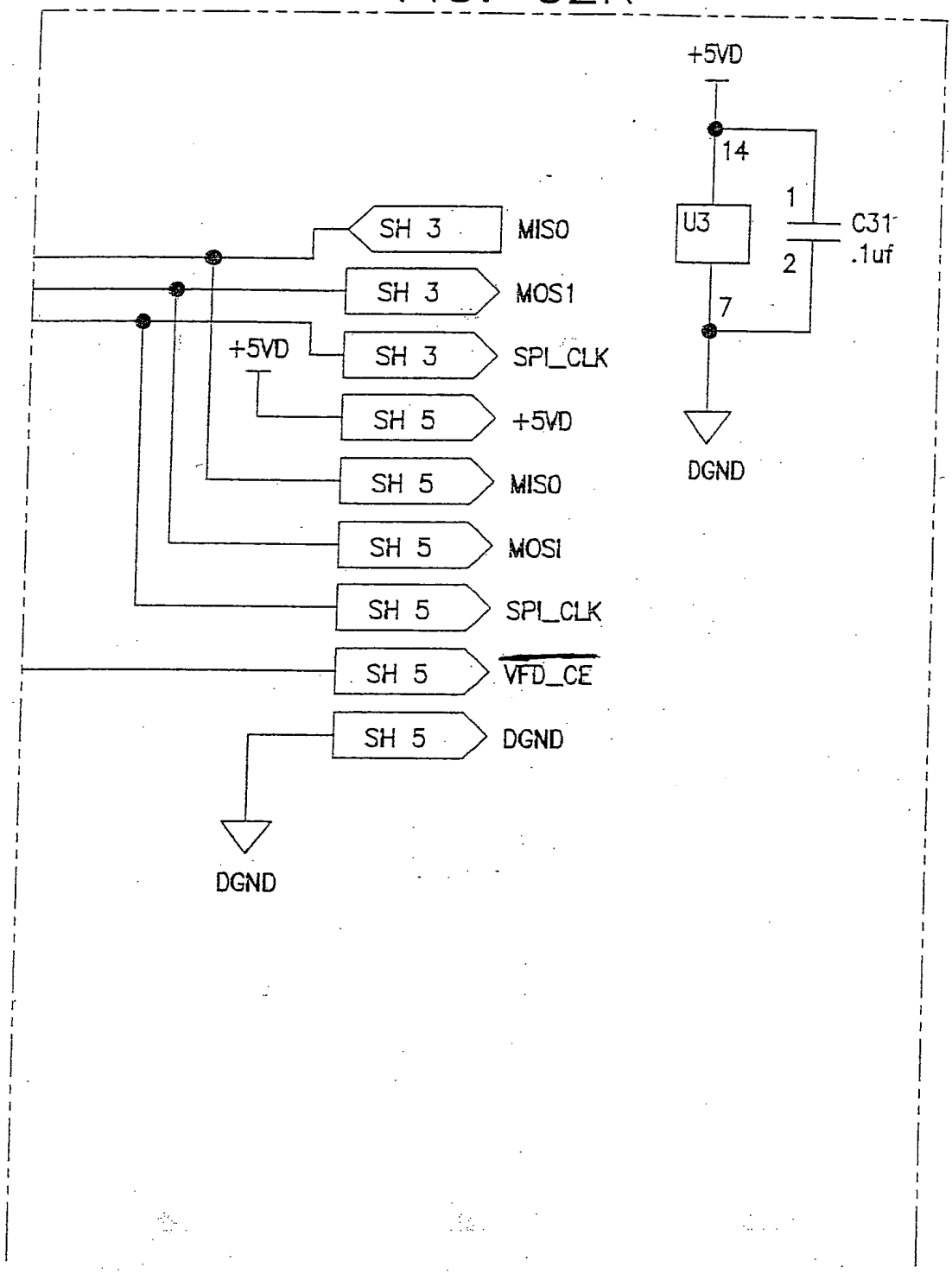
FIG. 62R



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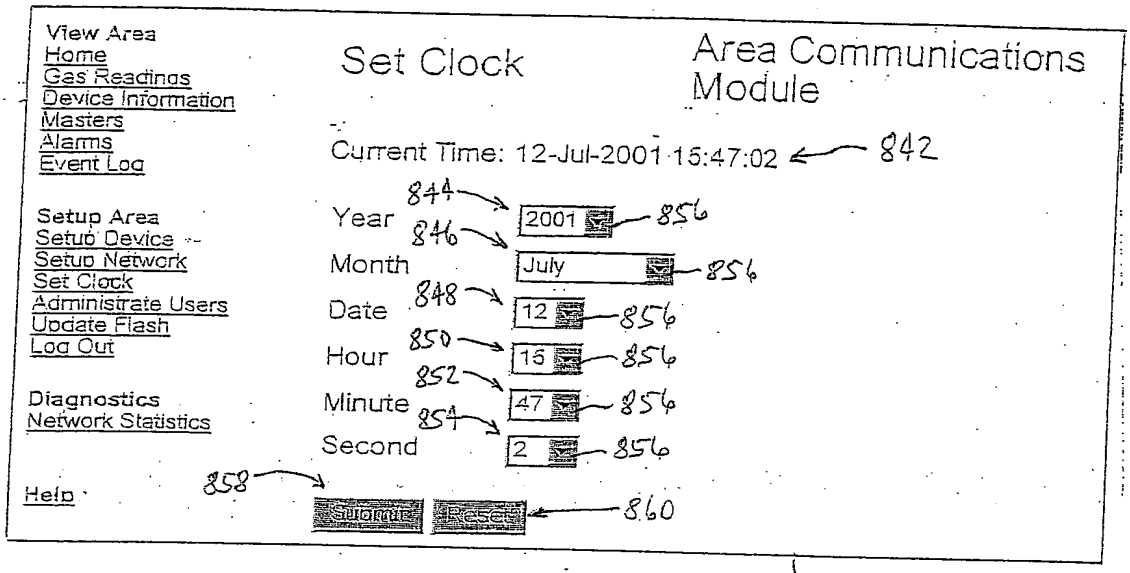

Fig. 55

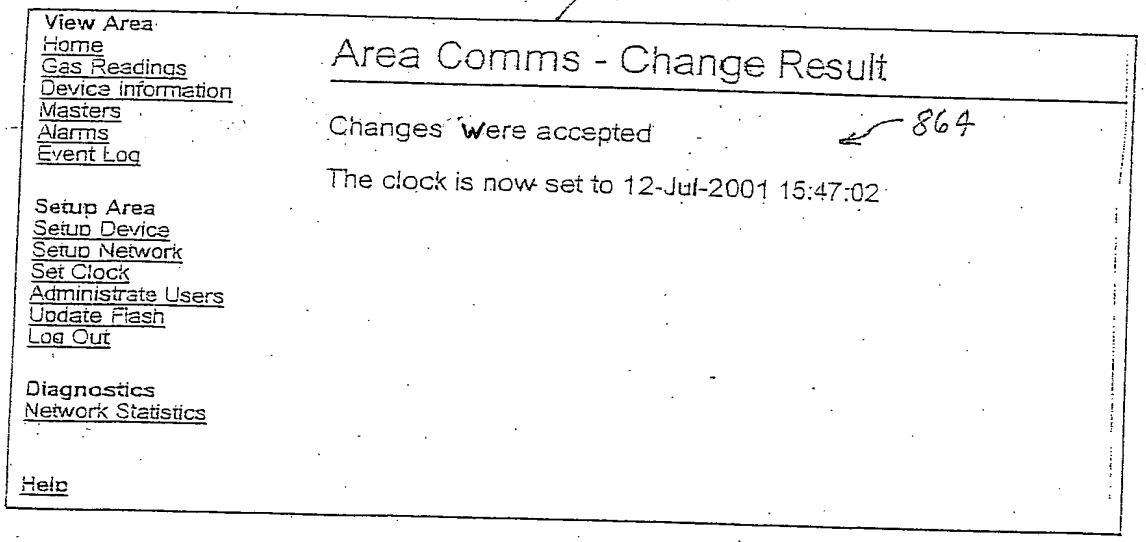

Fig. 56

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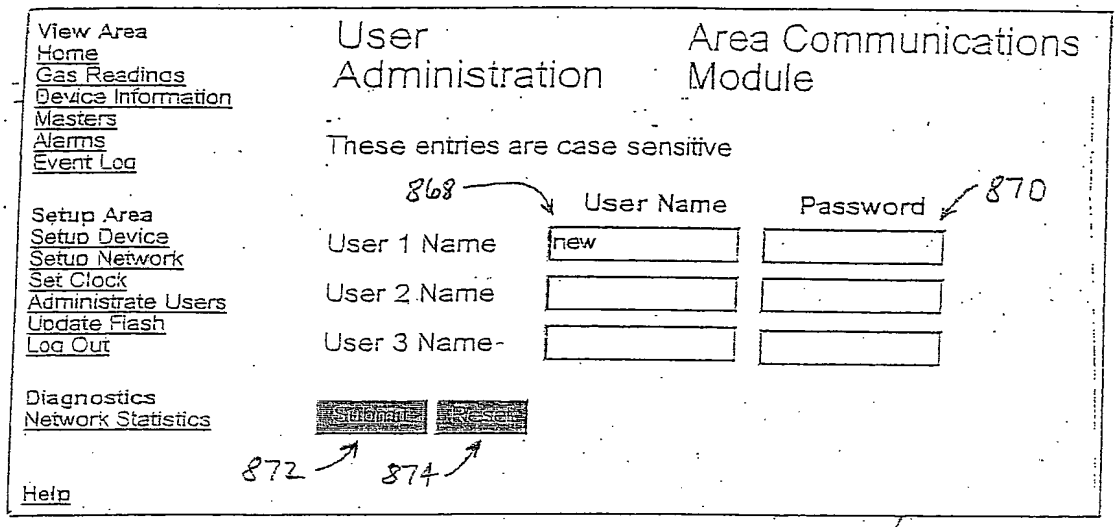

Fig. 57

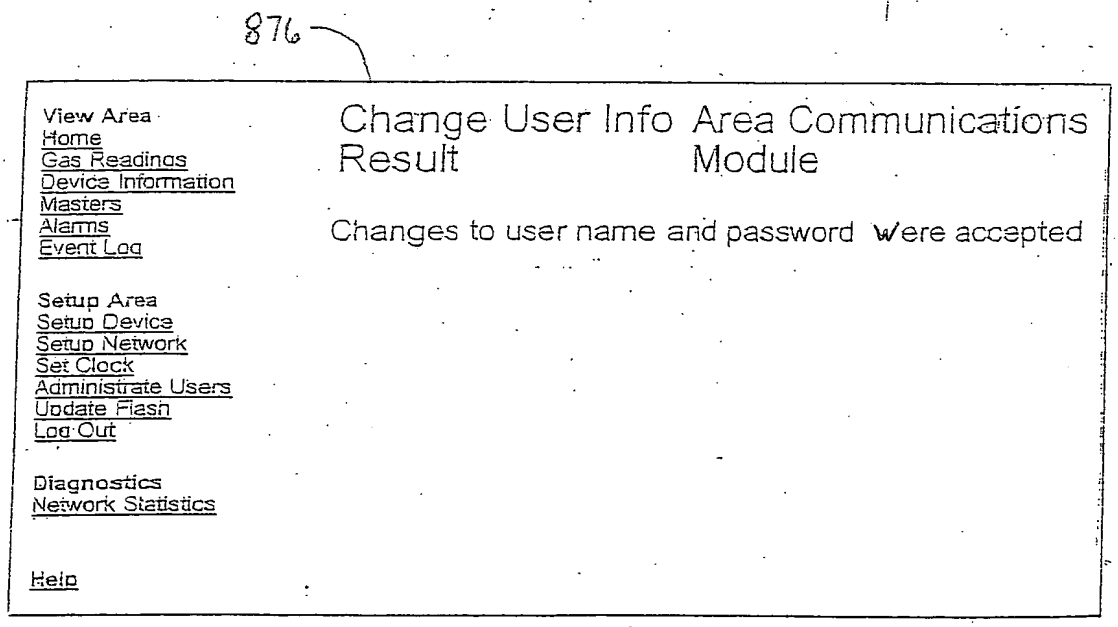

Fig. 58

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FIG. 62E

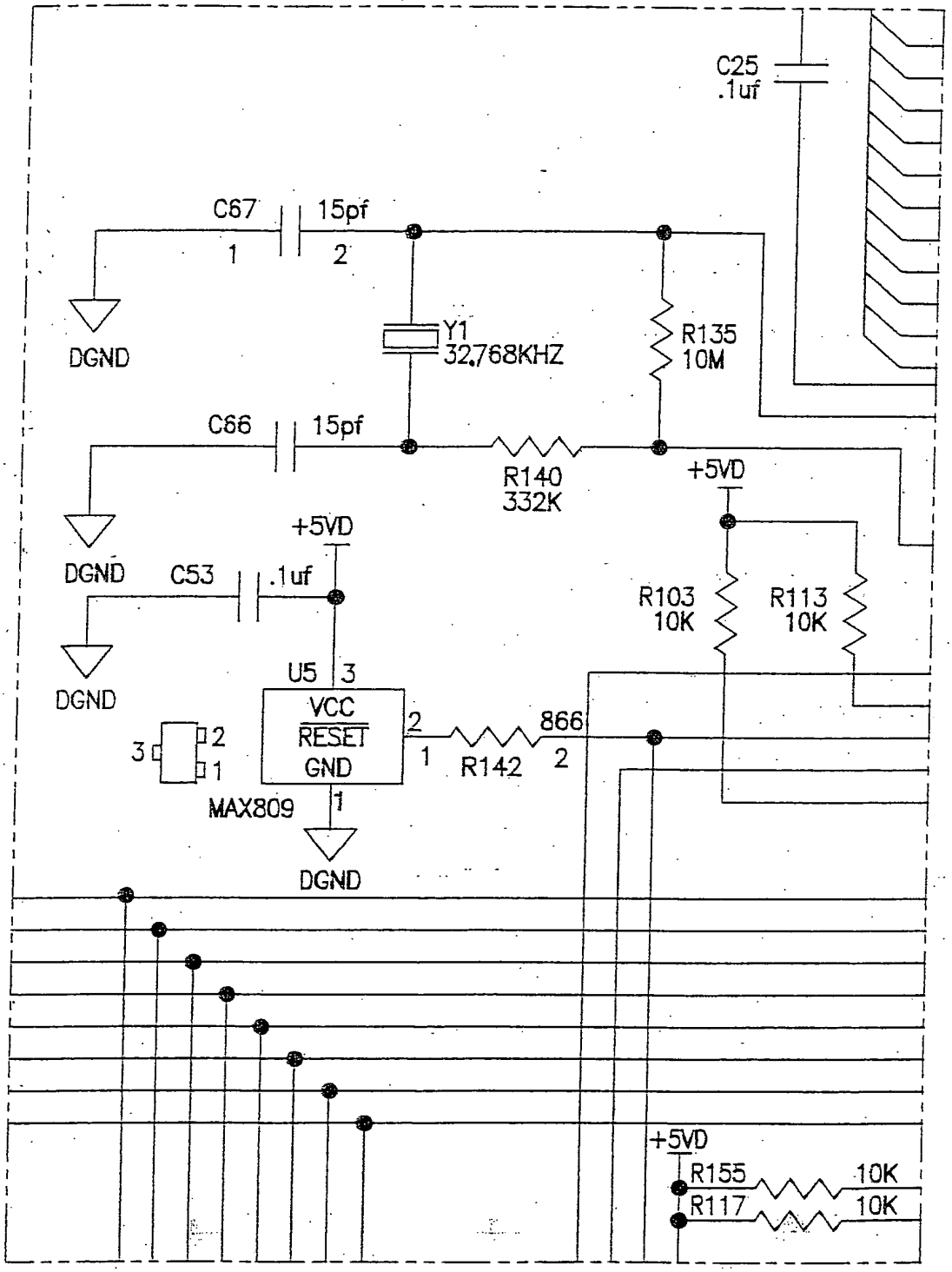

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FIG. 62G

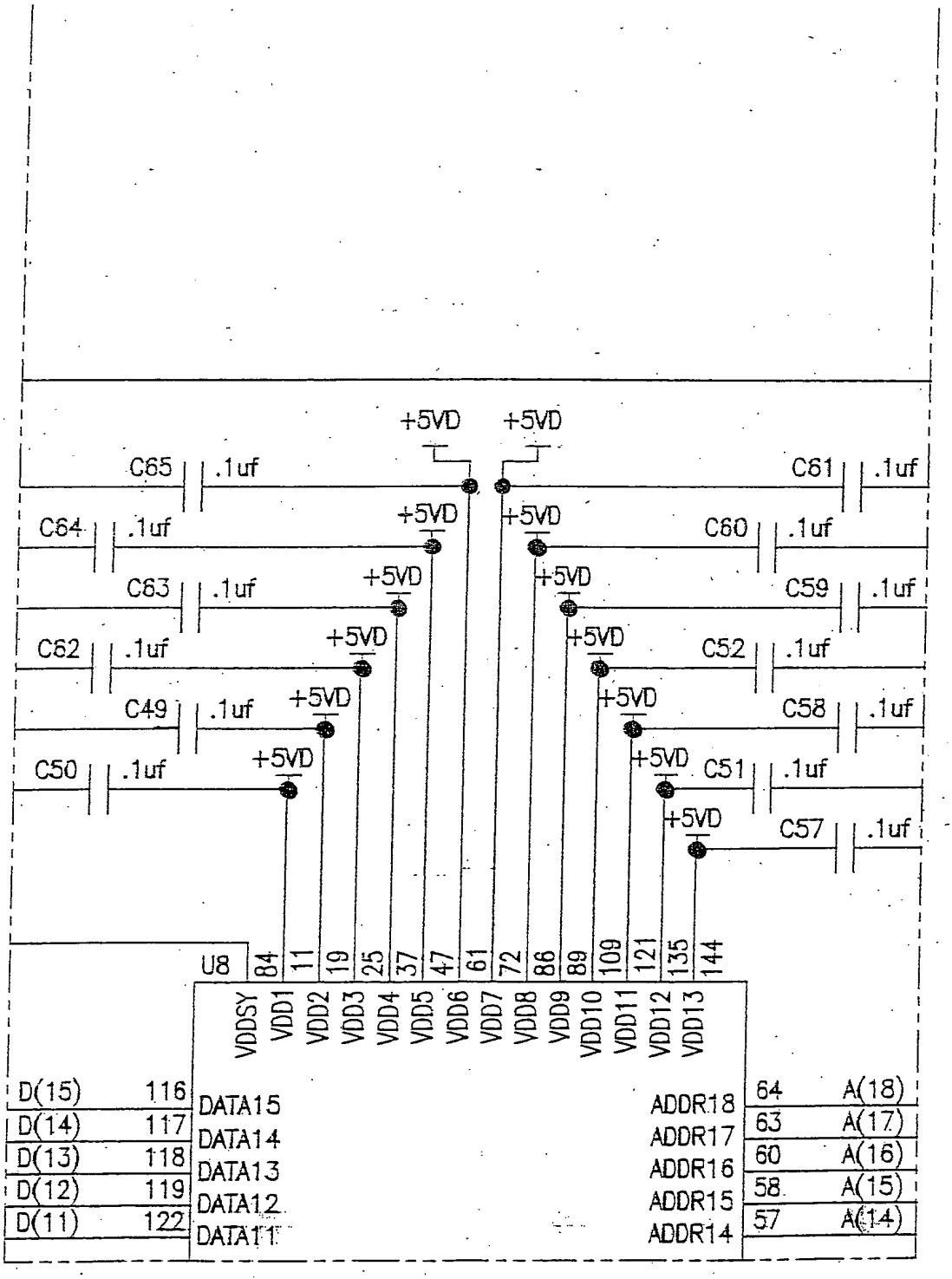

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FIG. 62H

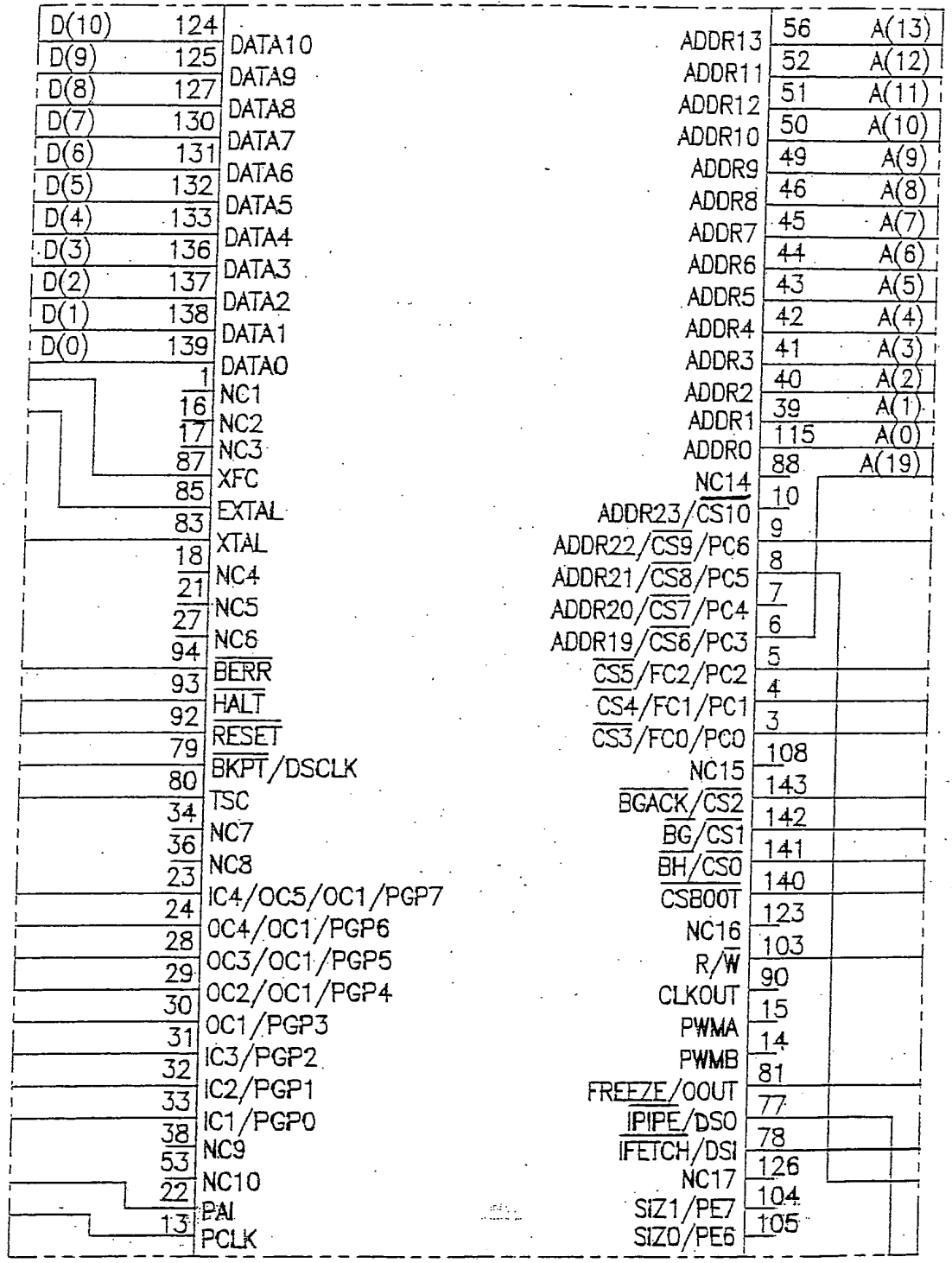

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FIG. 621

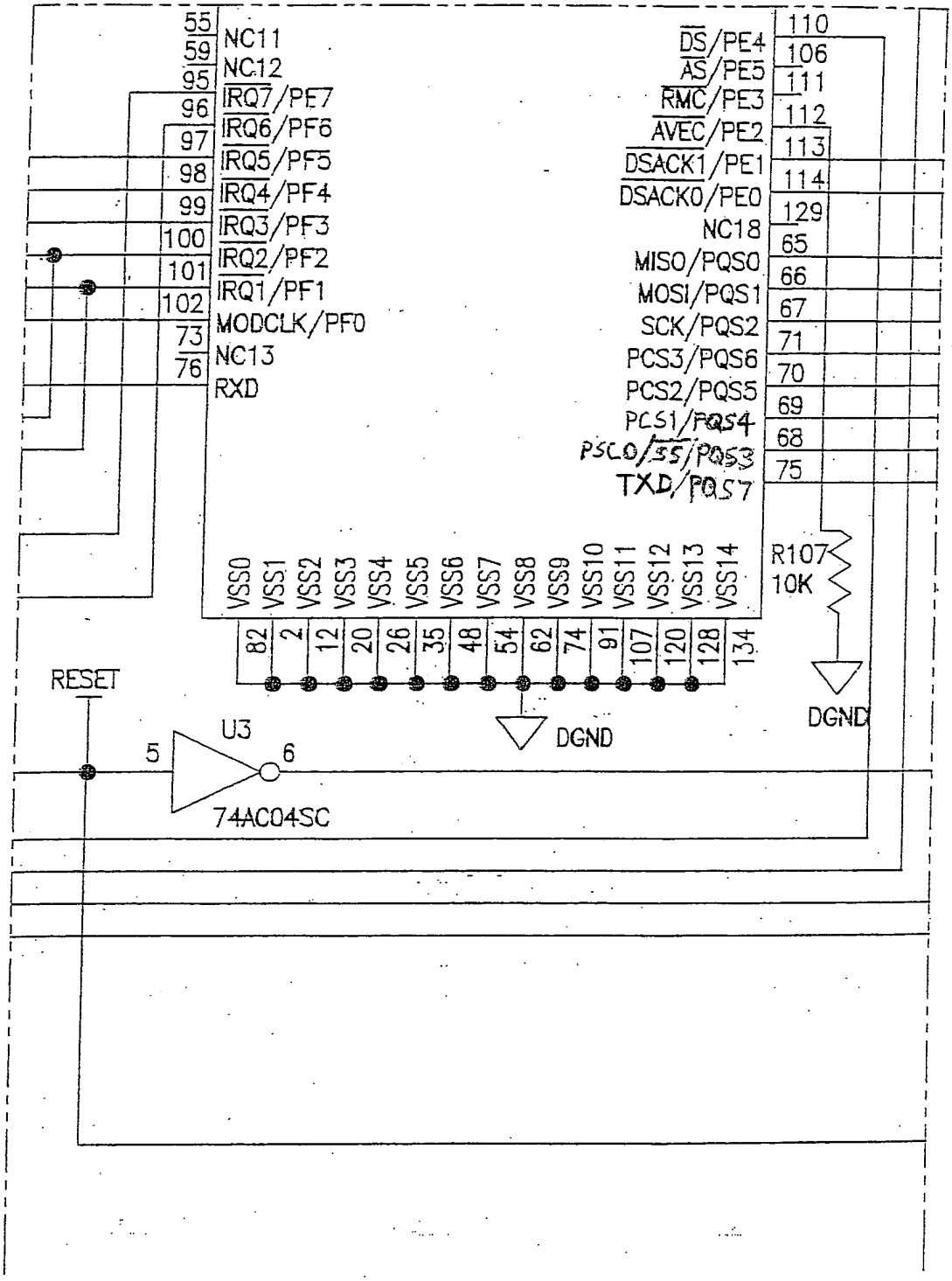

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FIG. 63F

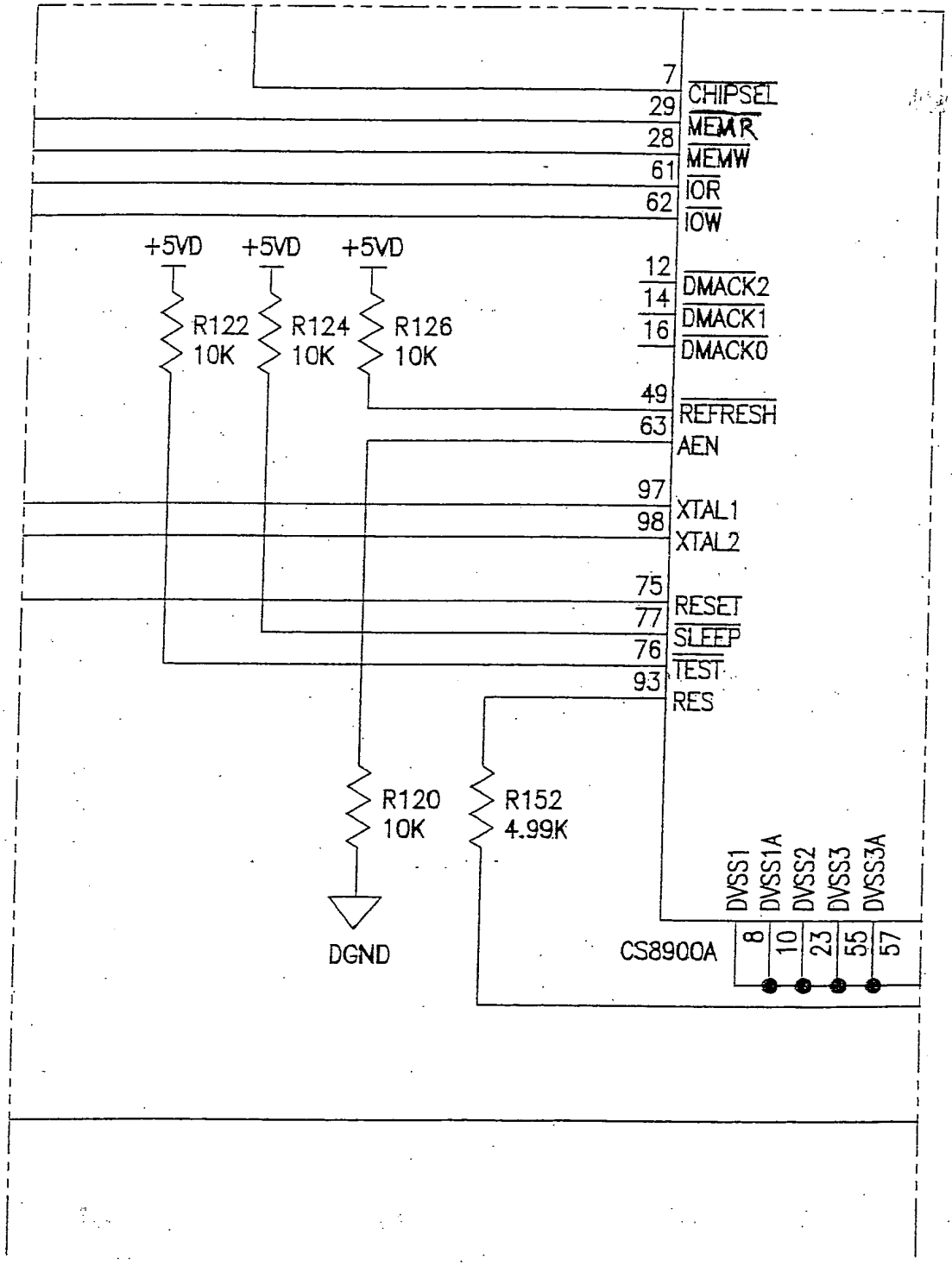

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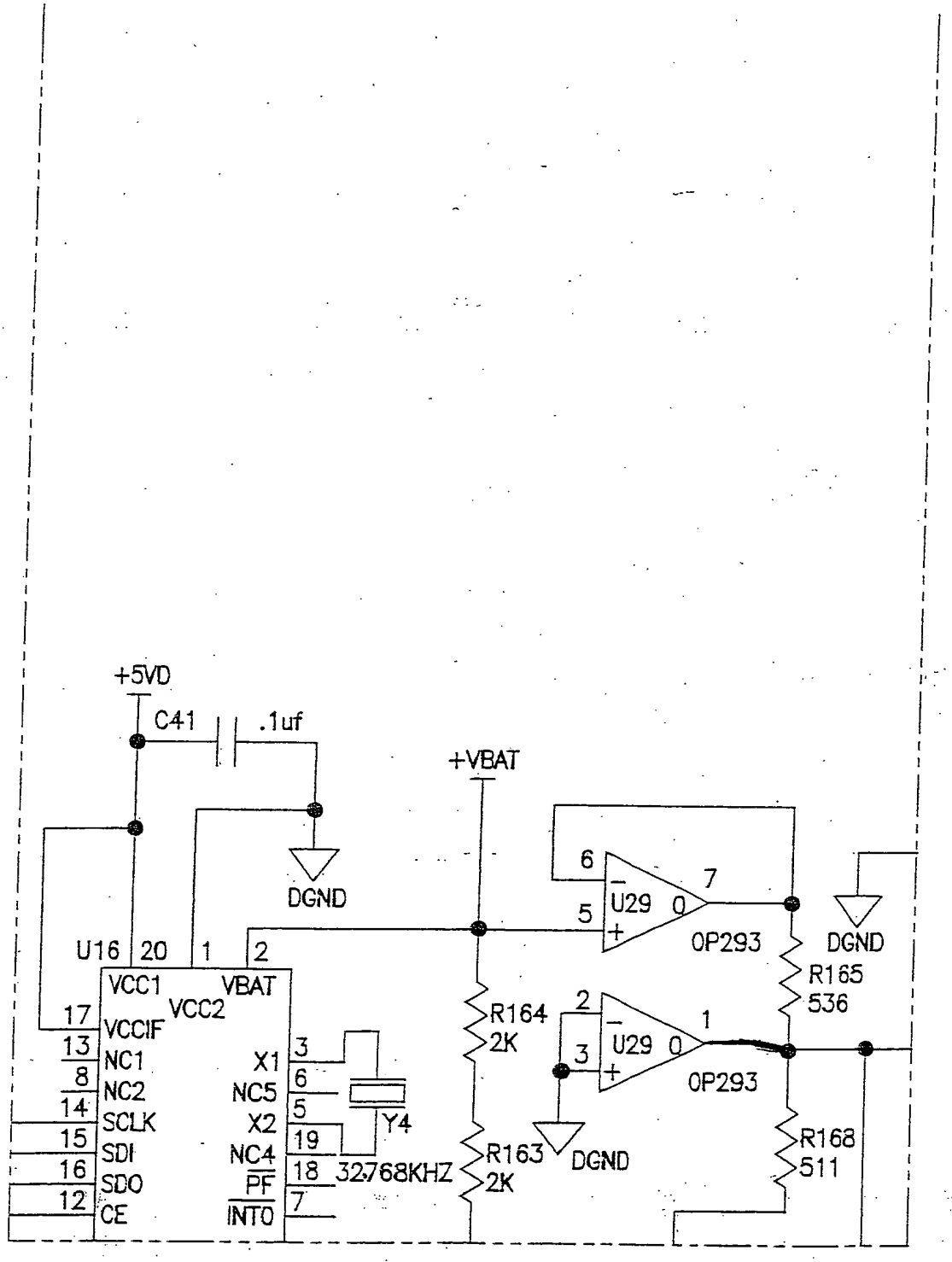
FIG. 64M



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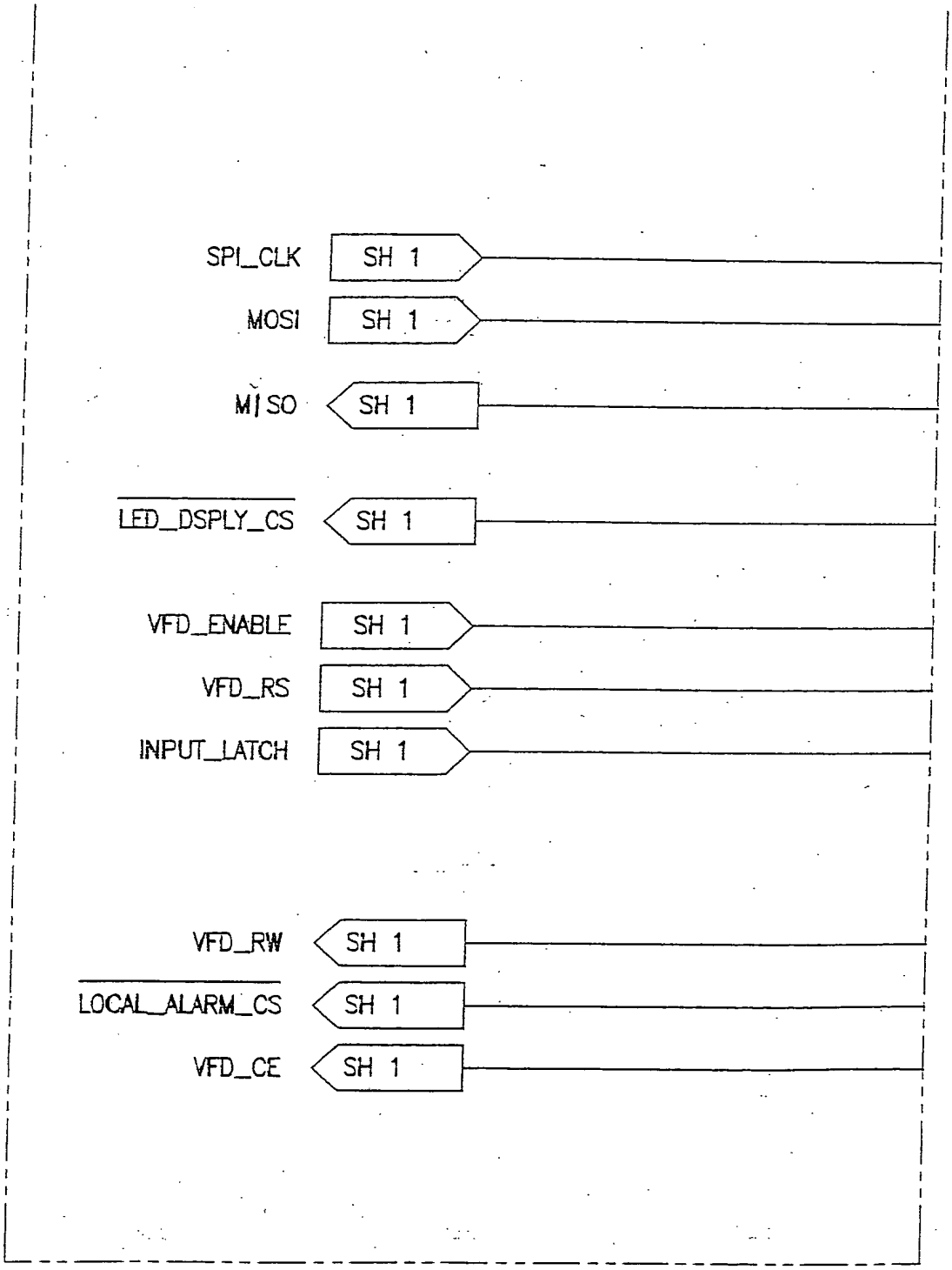
FIG. 66J